

## ABSTRACT OF THE DISCLOSURE

An interrupt vector is stored in a bank B of a dual operation flash memory, and an alternate interrupt vector corresponding to the interrupt vector is stored in a bank A. During rewrite processing of the bank B, if a CPU accesses the interrupt vector, an interrupt vector address conversion circuit converts the address of the interrupt vector to the address of the alternate interrupt vector, to access the memory. As a result, the interrupt vector data can be obtained to start the interrupt processing.

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